



Shri Mata Vaishno Devi University

Kakryal, Katra- 182320 (J&K)

Print Date 18/11/2018 11:15:10 PM

School of Electrical Engineering

Time Table (Major Examinations)

Semester 1 - B.Tech EE

November - Odd Semester - 2018-19

Date / Day	Time	Subject / Title	Room No.	Student Entry No.	Invigilator
22/11/2018 Thursday	09:30 AM - 12:30 PM	Engineering Mathematics (MTL 1012)	Hall - D 103	,18BEE001-18BEE037	Mr. Sanjay Kumar Sharma
24/11/2018 Saturday	09:30 AM - 12:30 PM	Introduction to C Programming (CSL 1022)	Hall - D 103	,18BEE001-18BEE037	Mr. Manoj Kumar
26/11/2018 Monday	02:00 PM - 05:00 PM	Basic Electrical Engineering (ECL 1020)	Hall - D 103	,18BEE001-18BEE037	Dr. Arjun Tyagi
27/11/2018 Tuesday	09:30 AM - 12:30 PM	Engineering Physics (PHL 1012)	Hall - D 103	,18BEE001-18BEE037	Dr. Sunil Kumar Wanchoo
28/11/2018 Wednesday	02:00 PM - 05:00 PM	Introduction to Electronics (ECL 1001)	Hall - D 103	,18BEE001-18BEE037	Dr. Partha Pratim Pratim
29/11/2018 Thursday	02:00 PM - 05:00 PM	Professional Communication (LNL 1411)	Hall - D 103	,18BEE001-18BEE037	Dr. Simranpreet Kaur
30/11/2018 Friday	02:00 PM - 05:00 PM	Engineering Graphics (MEP 1115)	Hall - D 103	,18BEE001-18BEE037	Dr. Ankush Anand