



श्री माता वैष्णो देवी विश्वविद्यालय

**Shri Mata Vaishno Devi University**

Sub Post office-182320, Jammu & Kashmir  
(Recognized under Section 12 (B) & 2 (f) of UGC Act, 1956)

SMVDU/R&D/23/2213-3217

Dated: 11.07.2023

**NOTIFICATION**

**Subject: Constitution of KAPILA Committee.**

As approved by the Competent Authority, a committee comprising of the following members is hereby constituted, as mandated by the "Kalam Program for IP Literacy and Awareness (KAPILA)" launched by the Ministry of Education's Innovation Cell (MIC) in collaboration with the All India Institute of Technical Education (AICTE):

Sl.	Name	Remarks
1.	Dr. Baijnath Kaushik, Associate Professor, SoCSE, SMVDU	IPR Experts
2.	Dr. Sanjay Mohan, Assistant Professor, SoME, SMVDU	
3.	Dr. Sanjeev Anand, Assistant Professor	SMVDU Alumni / Industry Experts
4.	Dr. Yatheshth Anand, Assistant Professor	
5.	Mr. Vivek Verma, Assistant Registrar (Legal Wing)	Legal Advisor

The above committee shall be responsible for registration of the University and uploading of the eligible patent applications and requisite documents on the KAPILA portal for reimbursement of the Filing and Examination fee, as per the guidelines of the Scheme. The committee shall also be responsible for the evaluation and scrutiny of patent applications of the University.

This issues with the approval of the Hon'ble Vice-Chancellor.

  
16/07/23  
Registrar

**Copy to:**

1. Finance Officer, SMVDU for information.
2. Dean (R&D) for information.
3. Concerned Committee Members for information and compliance.
4. Faculty I/c Website for uploading on the University website.
5. AR(VC Secretariat) for kind information of the Hon'ble Vice-Chancellor.
6. Concerned file.