



## श्री माता वैष्णो देवी विश्वविद्यालय

### SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 20BEC015 Ayush Arjariya of B. Tech. (ECE) Electronics & Communication  
Engineering 5<sup>th</sup> Semester Batch 2020 August - December 2022 (Odd semester)

Date: 04/08/2023

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
20BEC015	AYUSH ARJARIYA	5	7.08	0	76	* ECC 3002 Summer Internship - II( 1) * EED 3990 Minor Project-I (Hardware Based) ( 2) * ECL 2041 Antenna & Wave Propagation( 3) * ECL 2061 Microprocessor & Interfacing( 3) * ECL 2074 Digital System Design using Verilog( 3) * ECL 3080 Embedded Electronics & Microcontrollers( 3) * ECL 3090 Control Systems( 3) * ECL 4160 Satellite Communication( 3) * ECL 4202 Digital Signals Processing( 3) * ECP 2074 Verilog Lab( 1) * ECP 3081 Embedded Systems & Microcontrollers Lab( 1) * ECP 4203 Digital Signals Processing Lab( 1) * PCN 3079 Constitution of India( 0)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University  
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

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AR (Exams)

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