श्री माता वैष्णो देवी विश्वविद्यालय
SHRI MATA VAISHNO DEVI UNIVERSITY
Kakryal, Katra-182320, Jammu & Kashmir
Recognized under Section 2(f) & 12(B) of the UGC Act, 1956
registrar@smvdu.ac.in

विज्ञानं ब्रह्म

Ref. No.: SMVDU/AA/21/465-468   Date: 28th April, 2021
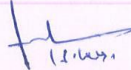
Notification

Sub: Grant of permission for equivalence courses against the backlog subjects.

With reference to the recommendation of the Committee of School of Electronics & Communication Engineering, duly forwarded by Head, SoECE and as approved by the Competent Authority, sanction is hereby conveyed for the grant of permission for consideration of equivalence of courses against the backlog for the following subjects.

Old Course (Backlog)	Present equivalent revised course
Basics Electrical Engineering (ECL 1020) and its associated Lab	Fundamental of Electrical Engineering (EEL 1006) and its associated Lab

Dean (Academic Affairs)

Copy to:

1. Dean, FoE, for information and compliance
2. Head, SoECE, for information and compliance.
3. PS to VC, for information of the Hon'ble Vice Chancellor.
4. I/c Faculty website, for uploading on SMVDU website.