

Shri Mata Vaishno Devi University

Kakryal, Katra-182320 (J&K) INDIA (A State University recognized u/s 2(f) & 12(B) of UGC)

Ref. No. SMVDU/R&D/22/10063 - 10067

Dated: 07 .10.2022

Notification

Subject: Constitution of Committee for preparation and submission of detailed proposal to Department of IT, JKUT for setting up of Centre of Excellence/Fab Lab at SMVDU.

As approved by the Competent Authority, a committee comprising of the following members is hereby constituted for preparation and submission of detailed proposal to Department of IT, JKUT for setting up of Centre of Excellence/Fab Lab for Integration of Artificial Intelligence and Machine Learning with IoT at SMVDU:

1.	Dr. Baijnath Kaushik, Associate Professor, SoCSE	-	Coordinator
2.	Dr. Sunanda, Assistant Professor, SoCSE		Co- Coordinator
3.	Dr. Naveen Gondhi, Assistant Professor, SoCSE		Co- Coordinator
4.	Dr. Vijay Kumar Sharma, Assistant Professor, SoECE	1 1 Jan	Co- Coordinator
5.	Dr. Sachin Kumar Gupta, Assistant Professor, SoECE	-	Co- Coordinator
6.	Dr. Sanjeev Anand, Assistant Professor, SoEM	-	Co- Coordinator

This issues in pursuance to the approval of the Competent Authority.

Registrar

Copy to:

- 1. All Deans/Heads, for information.
- 2. All concerned, for information & compliance.
- 3. PS to VC, for the kind information of Hon'ble Vice Chancellor.
- 4. I/c Website for uploading of the same on the University Website.
- 5. Concerned file.