

## श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K)
(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

No. SMVDU/R&D/23/ | 525-1532

Dated: 19.04.2023

## **NOTIFICATION**

It is hereby notified that the Ph.D. thesis of Mr. Arjun Puri (Entry No. 17DCS003), Ph.D. student, School of Computer Science & Engineering, Faculty of Engineering, entitled "Improved Techniques for Handling class imbalance and missing values in datasets" which was submitted under the supervision of Dr. Manoj Kumar Gupta, Associate Professor, School of Computer Science & Engineering; for the award of Ph.D. degree, has been accepted on the recommendations of Ph.D. Thesis Defense Committee of the University on 11.04.2023, and he is declared eligible for the award of Ph.D. degree w.e.f. 12.04.2023.

The degree will be conferred on him in the next Convocation of the University.

## Copy to:

- 1. I/c Dean FoE, for information.
- 2. Head SoCSE, for information.
- 3. Finance Officer, for information.
- **4.** Dr. Manoj Kumar Gupta, Associate Professor, School of Computer Science & Engineering, for information.
- **5.** Assistant Registrar, Vice Chancellor's Secretariat, for kind information of the Hon'ble Vice Chancellor.
- **6.** Faculty I/c Central Library, for information & uploading the final version of thesis on Shodh Ganga website.
- 7. Faculty I/c Website, for uploading on University Website.
- 9. Concerned file/Notification file.