



**श्री माता वैष्णो देवी विश्वविद्यालय**  
**SHRI MATA VAISHNO DEVI UNIVERSITY**  
**Revised Result (8<sup>th</sup> & 9<sup>th</sup> semesters) & Declaration of (10<sup>th</sup> & 11<sup>th</sup> semesters) of**  
**17BEC045 Rishesh Bhat of B Tech (ECE)**  
**Date: 13-12-2022**

| Entry No. | Student Name  | Sem | CGPA | SGPA | Credits Earned | Backlog List  |
|-----------|---------------|-----|------|------|----------------|---|
| 17BEC045  | RISHEESH BHAT | 11  | 6.58 | 7    | 172            | Nil   |
| 17BEC045  | RISHEESH BHAT | 10  | 6.58 | 8    | 171            | * ECP 1021 Electrical & Electronics Lab( 1)   |
| 17BEC045  | RISHEESH BHAT | 9   | 6.48 | 7.04 | 160            | * ECP 1021 Electrical & Electronics Lab( 1)   |
| 17BEC045  | RISHEESH BHAT | 8   | 6.39 | 7.4  | 137            | * ECP 1021 Electrical & Electronics Lab( 1) *<br>PCL 2042 Introduction to Logic( 3) |

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University  
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam