

श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 19BEC080 Sarvesh of B. Tech. (ECE) Electronics & Communication Engineering 5th Semester Batch 2019 August - December 2021 (Odd semester)

Date: <u>13/12/2022</u>

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
19BEC080	SARVESH	5	6.64	5	85	* ECC 3002 Summer Internship - II(1) * ECD 3992 Major Project-I (Hardware Based Project)(2) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2061 Microprocessor And Interfacing(3) * ECL 2152 Digital Communication Engineering(3) * ECL 3081 Embedded Systems & Microcontollers(3) * ECP 2030 Linear Integrated Circuits Lab(1) * ECP 2061 Microprocessor & Interfacing Lab(1) * ECP 2152 Digital Communication Engineering Lab(1)

ERRORS & OMMISSIONS EXCEPTED

Any query or clarification may be sought from University But within six months of the date of declaration of this result

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