



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of B. Tech. (ECE) Electronics & Communication Engineering 3rd Semester Batch 2020
August - December 2021 (Odd semester)

Date: **31/05/2022**

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
20BEC002	ADITYA SINGH	3	7.67	7.19	57	* MEL 1039 Engineering Graphics with CAD(2)
20BEC005	AMANDEEP SINGH	3	7.88	7.91	59	Nil
20BEC021	DIVYARTH SINGH	3	6.75	6.7	56	* ECC 2001 Summer Internship - I(1) * MEL 1039 Engineering Graphics with CAD(2)
20BEC046	PUNEET YADAV	3	7.46	7.17	59	Nil
20BEC051	RAJWARDHAN SINGH	3	7.61	7.19	59	Nil
20BEC092	VINAYAK PARTH SARTHI	3	6.64	6.67	55	* CSP 1028 Programming Using Python Lab(2) * MEL 1039 Engineering Graphics with CAD(2)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam