



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of 16BEC062 (Backlog student) of B. Tech. ECE (Electronics & Communication Engineering) Batch 2016 for the semesters (withheld earlier)

Date: 04/02/2022

Entry no	Student Name	Sem	CGPA	SGPA	Credit Earned	Backlog List
16BEC062	SUKRITI MAHAJAN	5 (appeared for her backlog courses of 3 rd semester)	5.34	4.8	61	* ECL 1022 Network Analysis & Synthesis(4)
16BEC062	SUKRITI MAHAJAN	6 (appeared for her backlog courses of 4 th semester)	5.3	2.7	75	* ECL 1022 Network Analysis & Synthesis(4) * ECL 2041 Antenna & Wave Propagation(3) * ECL 2060 Microprocessor Systems(4) * ECL 2071 Digital System Design using VHDL(5)
16BEC062	SUKRITI MAHAJAN	7 (appeared for her backlog courses of 5 th semester)	5.48	4.64	91	* ECL 1022 Network Analysis & Synthesis(4) * ECL 2041 Antenna & Wave Propagation(3) * ECL 2060 Microprocessor Systems(4) * ECL 2071 Digital System Design using VHDL(5) * ECL 3050 Microwave Engineering(4) * ECL 3090 Control Systems(4)
16BEC062	SUKRITI MAHAJAN	8 (appeared for her backlog courses of 6 th semester)	5.74	6.55	120	* ECL 1022 Network Analysis & Synthesis(4) * ECL 2071 Digital System Design using VHDL(5) * ECL 3050 Microwave Engineering(4) * ECL 3090 Control Systems(4)
16BEC062	SUKRITI MAHAJAN	9 (appeared for her backlog courses of 7 th semester)	6.07	7.32	151	* ECL 1022 Network Analysis & Synthesis(4) * ECL 2071 Digital System Design using VHDL(5)
16BEC062	SUKRITI MAHAJAN	10 (appeared for her backlog courses of 8 th semester)	6.34	8.53	170	Nil

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/-

Sd/-

Sd/-

Sd/-

J. A / S. O (Exam)

AR (Exam)

Faculty Coordinator

Faculty I/c Exam