



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of 20BEC019 Dev Singh Sambyal, 20BEC030 Jatin Manhotra & 20BEC053 Rinitesh of B. Tech. (ECE) Electronics & Communication Engineering 5th Semester Batch 2020

August - December 2022 (Odd semester)

Date: **28/04/2023**

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
20BEC019	DEV SINGH SAMBYAL	5	6.03	3.29	78	* ECC 2001 Summer Internship - I(1) * ECL 2030 Linear Integrated Circuits & Applications(3) * ECL 2040 Electromagnetic Field Theory(3) * ECL 2041 Antenna & Wave Propagation(3) * ECL 2061 Microprocessor And Interfacing(3) * ECL 2074 Digital System Design using Verilog(3) * ECL 3081 Embedded Systems & Microcontrollers(3) * ECL 3090 Control Systems(3) * ECP 2074 Verilog Lab(1) * ECP 2152 Digital Communication Engineering Lab(1) * ECP 2201 Electronics Workshop Lab (1)
20BEC030	JATIN MANHOTRA	5	6.83	6.31	103	Nil
20BEC053	RINITESH	5	7.23	7.24	101	* MEL 1039 Engineering Graphics with CAD(2)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams