



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of 20BEC093 Yash Anand Giri of B. Tech. (ECE) Electronics & Communication
Engineering 5th Semester Batch 2020 August - December 2022 (Odd semester)

Date: 27/03/2023

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
20BEC093	YASH ANAND GIRI	5	6.64	3.71	90	* ECL 2041 Antenna & Wave Propagation(3) * ECL 2061 Microprocessor And Interfacing(3) * ECL 2074 Digital System Design using Verilog (3) * ECL 3081 Embedded Systems & Microcontrollers(3) * ECP 2074 Verilog Lab(1)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exams)

Sd/-

AR (Exams)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exams