

श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

SHRI MATA VAISHNO DEVI UNIVERSITY
Result of B. Tech. (EE) Electrical Engineering 7th Semester Batch 2019
August - December 2022 (Odd semester)

Date: 27-06-2023

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
19BEE030	RONAKPREET KOUR BALI	7	6.57	5.38	144	* EEL 1006 Fundamentals of Electrical Engineering(4) * MEL 1039 Engineering Graphics with CAD(2)
19BEE031	SACHIN JIJ	7	5.88	5.45	143	* EEE 5005 HVDC Transmission System(3) * EEL 3632 Power System-II(4)

ERRORS & OMMISSIONS EXCEPTED

Any query or clarification may be sough

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/- Sd/- Sd/- Sd/-

J. A / S. O (Exams) AR (Exams) Faculty Coordinator Faculty I/c Exams