



**श्री माता वैष्णो देवी विश्वविद्यालय**  
**SHRI MATA VAISHNO DEVI UNIVERSITY**  
**Result of B. Tech. (EE) Electrical Engineering 9<sup>th</sup> Semester Batch 2018**  
**August - December 2022 (Odd semester)**  
**Date: 24-05-2023**

Entry no	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog
18BEE030	SHUBHAM KUMAR GUPTA	9	7.22	7.5	146	* CSL 1028 Programming using Python( 4) * ECL 1010 Basic Electronics( 4) * ECL 1022 Network Analysis & Synthesis( 4) * LNP 1142 Language Lab( 1) * MEL 1012 Engineering Mechanics( 4) * MEP 1114 Workshop( 1) * MTL 1026 Engineering Mathematics-II( 3) * PCL 1067 Discourse on Human Virtues( 3)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University  
but within six months of the date of declaration of this result

Sd/-

Sd/-

Sd/-

Sd/-

J. A / S. O (Exams)

AR (Exams)

Faculty Coordinator

Faculty I/c Exams