



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of B. Tech. (ECE) 5th Semester Batch 2018 (July - December 2020) (Odd semester)

Date: 01/12/2021

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
18BEC005	ABHISHEK SINGH ANAND	5	6.36	7.29	95	* ECL 1022 Network Analysis & Synthesis(4) * ECL 1030 Electronic Circuits & Simulation(4) * ECL 2070 Digital Electronics(5) * MEP 1116 Engineering Graphics(2) * MTL 1026 Engineering Mathematics-II(3)
18BEC008	AMAN SHARMA	5	6.13	7.08	76	* CSL 1028 Programming using Python(4) * ECL 1001 Introduction to Electronics Engineering(1) * ECL 1022 Network Analysis & Synthesis(4) * ECL 1030 Electronic Circuits & Simulation(4) * ECL 2040 Electromagnetic Field Theory(4) * ECL 2070 Digital Electronics(5) * ECL 2151 Analog Communication Engineering(5) * LNP 1142 Language Lab(1) * MEP 1043 Engineering Workshop(1) * MEP 1116 Engineering Graphics(2) * MTL 1026 Engineering Mathematics-II(3) * PCL 2042 Introduction to Logic(3) * PCN 1010 NSS(0)
18BEC010	ANUDEEP KOUL	5	6.12	7.59	104	* ECL 1030 Electronic Circuits & Simulation(4) * ECL 2151 Analog Communication Engineering(5)
18BEC050	RJ NIKHIL SINGH	5	6.36	7.47	113	Nil

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

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Faculty I/c Exam