



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of B. Tech. ECE (Electronics & Communication Engineering) Batch 2017 8th Semester
Jan - May 2021 (Even semester) along with 03 backlog students of Batch 2015 & 2016

Date: 08-10-2021

Entry No	Student Name	Sem	CGPA	SGPA	Credit Earned	Backlog List
15BEC014	NAVITA	12	6.36	7	170	Nil
16BEC005	ABHINAV SLATHIA	10	6.24	8	170	Nil
16BEC068	VASU SOI	10	5.61	8	170	Nil
17BEC010	ARYAN MAHAJAN	8	7.33	9	170	Nil
17BEC010	ARYAN MAHAJAN	7	7.22	8	160	Nil
17BEC010	ARYAN MAHAJAN	6	7.14	8.33	141	Nil
17BEC013	ASKANDHA SHARMA	8	7.64	8	170	Nil
17BEC021	GURNAIN SINGH	8	6.47	8.73	170	Nil
17BEC021	GURNAIN SINGH	7	6.25	7.66	155	* ECL 1030 Electronic Circuits & Simulation(4) * ECP 1030 Circuits & Simulation Lab(1)
17BEC039	NIREN DEV SINGH	8	6.75	8	170	Nil
17BEC043	RAJEEV KUMAR	8	8.12	9	170	Nil
17BEC052	SHIVAM SHARMA	8	6.25	8	170	Nil
17BEC052	SHIVAM SHARMA	7	6.14	7.71	160	Nil
17BEC052	SHIVAM SHARMA	6	5.81	6.7	132	* ECL 2151 Analog Communication Engineering(5) * ECL 3090 Control Systems(4)
17BEC053	SHRIYA	8	6.94	9	170	Nil
17BEC064	VIMARSH KOUL	8	6.42	9	170	Nil
17BEC065	VISHNU MADHAV	8	7.66	9	170	Nil

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University
but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam