



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Revised Result of B. Tech. (CSE) 5th Semester Batch 2018 (July - December 2020) (Odd semester)

Date: 09/12/2021

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
18BCS003	ABHIMANYU SARSWAT	5	6.25	7.45	116	* PCN 1010 NSS(0)
18BCS009	GUNUPUDI AJITH	5	6.12	7.65	110	* LNL 1411 Professional Communication(3) * MTL 2024 Discrete Structures(3)
18BCS016	ANKIT KUMAR	5	6.4	8.31	103	* CSL 2031 Data Structures(4) * MTL 1025 Engineering Mathematics-I(3) * MTL 1026 Engineering Mathematics-II(3) * PCL 2042 Introduction to Logic(3)
18BCS018	ANSHITA GUPTA	5	6.97	8.92	113	* MTL 2024 Discrete Structures(3)
18BCS024	BIJENDRA RAUT	5	6.17	8.14	116	Nil
18BCS033	HARSH GOSWAMI	5	7.15	7.89	115	MEP 1114 Workshop (1)
18BCS035	HARSH KUMAR	5	6.25	7.48	102	* CSL 2061 Computer Organization & Architecture(4) * ECL 2070 Digital Electronics(5) * MEP 1116 Engineering Graphics(2) * MTL 1026 Engineering Mathematics-II(3)
18BCS039	KULWANT SINGH DHAMA	5	6.91	8.13	109	* ECL 3061 Microprocessor & Interfacing(3) * MTL 2024 Discrete Structures(4)
18BCS041	MANOJ RAGHVAN M	5	5.98	6.95	113	* MTL 2024 Discrete Structures(3)
18BCS062	RISHABH MODI	5	7.18	8.52	116	Nil
18BCS069	SHASHIKANT SINGH	5	6.07	7.06	103	* CSL 2061 Computer Organization & Architecture(4) * ECL 2070 Digital Electronics(5) * ECL 3061 Microprocessor & Interfacing(4)
18BCS085	V AKSHAY VALLABH	5	6.88	8.67	116	* PCN 1010 NSS(0)

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam