



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of students of M. Tech (ECE) Batch 2020 1st Semester Nov, 2020 –Mar, 2021

(Odd semester AY 2020-21)

Entry No	Student Name	Sem	CGPA	SGPA	Credit Earned	Backlog List
20MMC001	ANKITA PANDITA**	1	7.24	6.91	21	* ECP 6221 Integrated Circuit Design Lab(1)
20MMC002	AYUSHE	1	8.14	8.14	22	Nil
20MMC003	JASPREET KOUR	1	6.79	5.86	19	* ECL 6221 Integrated Circuit Design(3)
20MMC004	KAVANDEEP KOUR	1	8.32	8.32	22	Nil
20MMC005	MIR NASHIT RABEET	1	7.55	7.55	22	Nil
20MMC006	MURTAZA WAHEED	1	8.09	8.09	22	Nil
20MMC007	NAIRA NAFEEES	1	9	0.41	1	* ECL 6110 Wireless Networks & Protocols(4) * ECL 6150 Advanced Digital Communications(4) * ECL 6182 Signal Processing Techniques & Applications(4) * ECL 6221 Integrated Circuit Design(3) * ECL 6301 Research Methodology for Electronics Engineers(4) *ECP 6110 Wireless Networks Lab(1) * ECP 6221 Integrated Circuit Design Lab(1)
20MMC008	NEENA VERMA	1	7	7	22	Nil
20MMC009	RASHIKA RAINA	1	8.45	8.45	22	Nil
20MMC010	SABA MOHI U DIN	1	8.59	8.59	22	Nil
20MMC012	SADAT RIYAZ	1	8.82	8.82	22	Nil
20MMC014	SHEFALI KOUL	1	6.73	6.73	22	Nil
20MMC015	TABINDHA FARAH	1	6.64	6.64	22	Nil
20MMC016	TUHA MOHD TEELI	1	0	0	0	* ECL 6150 Advanced Digital Communications(4) * ECL 6182 Signal Processing Techniques & Applications(4) * ECL 6221 Integrated Circuit Design(3) * ECL 6301 Research Methodology for Electronics Engineers(4) * ECP 6182 Signal Processing Lab(1) * ECP 6221 Integrated Circuit Design Lab(1)
20MMC017	VANSHIKA SHARMA	1	8.45	8.45	22	Nil
20MMC018	CHARIT CHANDRA	1	7.73	7.73	22	Nil
20MMC019	EISHRET JAVEED	1	6.9	6.59	21	* ECP 6221 Integrated Circuit Design Lab(1)
20MMC020	ITIKA	1	7.77	7.77	22	Nil

** Also applied for cancellation of admission

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Prepared by

Checked by

Verified by

Sign

J. A / S. O (Exam)

AR (Exam)

Faculty Coordinator

Faculty I/c Exam