



श्री माता वैष्णो देवी विश्वविद्यालय

SHRI MATA VAISHNO DEVI UNIVERSITY

Result of M. Tech. (ECE) 2nd Semester Batch 2020 March - July 2021 (Even semester)

Date: 21-10-2021

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
20MMC002	AYUSHE	2	8.28	8.47	39	Nil
20MMC003	JASPREET KOUR	2	7.03	7.29	36	* ECL 6221 Integrated Circuit Design(3)
20MMC004	KAVANDEEP KOUR	2	8.28	8.24	39	Nil
20MMC005	MIR NASHIT RABEET	2	7.82	8.18	39	Nil
20MMC006	MURTAZA WAHEED	2	8.26	8.47	39	Nil
20MMC007	NAIRA NAFEEES	2	7.17	7.06	18	* ECL 6110 Wireless Networks & Protocols(4) * ECL 6150 Advanced Digital Communications(4) * ECL 6182 Signal Processing Techniques & Applications(4) * ECL 6221 Integrated Circuit Design(3) * ECL 6301 Research Methodology for Electronics Engineers(4) * ECP 6110 Wireless Networks Lab(1) * ECP 6221 Integrated Circuit Design Lab(1)
20MMC008	NEENA VERMA	2	7.33	7.76	39	Nil
20MMC009	RASHIKA RAINA	2	8.38	8.29	39	Nil
20MMC010	SABA MOHI U DIN	2	8.36	8.06	39	Nil
20MMC012	SADAT RIYAZ	2	8.67	8.47	39	Nil
20MMC014	SHEFALI KOUL	2	6.95	7.24	39	Nil
20MMC015	TABINDHA FARAH KHAN QURESHI	2	7	7.47	39	Nil
20MMC017	VANSHIKA SHARMA	2	8.49	8.53	39	Nil
20MMC018	CHARIT CHANDRA TIWARI	2	7.79	7.88	39	Nil
20MMC019	EISHRET JAVEED	2	7.21	7.59	38	* ECP 6221 Integrated Circuit Design Lab(1)
20MMC020	ITIKA	2	8.18	8.71	39	Nil

ERRORS & OMISSIONS EXCEPTED

Any query or clarification may be sought from University but within six months of the date of declaration of this result

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam